## Notice of References Cited Application/Control No. | Applicant(s)/Patent Under Reexamination | 10/621,908 | DHONG ET AL. | Examiner | Art Unit | Benjamin P. Geib | 2181 | Page 1 of 1

## **U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-			
	В	US-			
	С	US-			
	D	US-			
	E	US-			
	F	US-			
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## **FOREIGN PATENT DOCUMENTS**

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## **NON-PATENT DOCUMENTS**

*	* Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U	"AltiVec Technology Programming Environments Manual"; Feb. 2002; Chapter 1 (pages 1-12) and Chapter 6 (pages 1-8, 170)			
	٧	"AltiVec: Bringing Vector Technology to the PowerPC Processor Family"; Tyler et al.; 1999; IEEE			
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

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